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S/N 10/553,860

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|-------------|---|-----------------|------------------|
| Applicant: | Nanai et al. | Examiner: | David J. Goodwin |
| Serial No.: | 10/553,860 | Group Art Unit: | 2818 |
| Filed: | October 20, 2005 | Docket No.: | 10873.1744USWO |
| Title: | FIELD EFFECT TRANSISTOR, ELECTRICAL ELEMENT ARRAY, AND MANUFACTURING METHOD FOR THE SAME | | |

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on August 11, 2007.

By:

Name:

Rong Yang

Carrie Vanderlinde

Mail Stop: **AMENDMENT**
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT AND RESPONSE

Dear Sir:

In response to the Office Action mailed May 13, 2008, please amend the above-identified application as follows:

Amendments to the Claims are shown in the listing of claims that begins on page 2 of this document.

Remarks/Arguments begin on page 4 of this document.

An **Appendix** including verified translation of Japanese priority application with serial No. 2003-409342 is attached following page 5 of this document.